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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lehmann, *et al.*

Attorney Docket: 00P09113 US

Filed: December 28, 2000

Examiner: Unknown

Serial No.: 09/751,474

Art Unit: 2811

Title: Area Efficient Stacking of Antifuses in Semiconductor Device

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This Information Disclosure Statement is submitted under 37 C.F.R. §1.97(c) together with a \$180.00 fee under 37 C.F.R. §1.17(p) after the C.F.R. §1.97(b) time period, but before final action or notice of allowance, whichever occurs first.

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Respectfully submitted,

Ira S. Matsil  
Attorney for Applicant  
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1

**Complete if Known**

Application Number	09/751,474
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Filing Date	December 28, 2000
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First Named Inventor	Lehmann, et al.
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Art Unit	2811
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Examiner Name	Unknown
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Attorney Docket Number | 00P09113 US

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

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Examiner Signature		Date Considered	
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Form PTO/SB/08A (1 page), 8 references cited, 1 included herewith  
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Respectfully submitted,

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Legal Assistant

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